

FIG.1

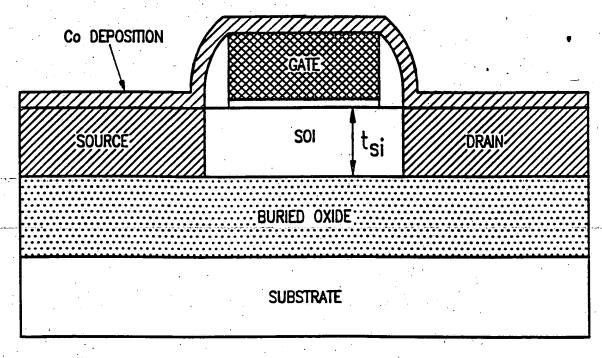


FIG.2

use 2rd set

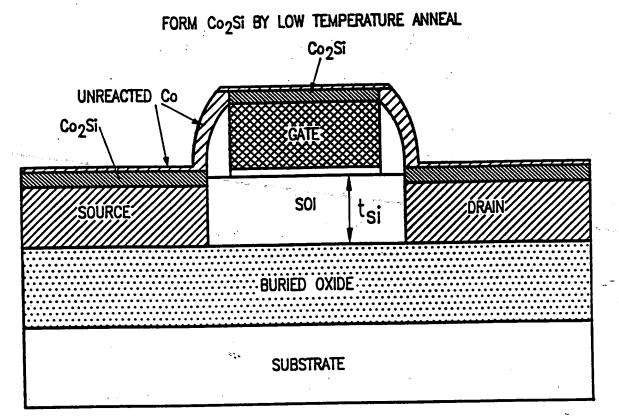


FIG.3

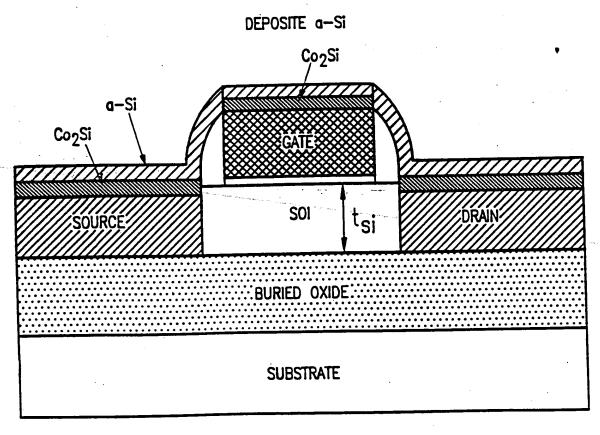
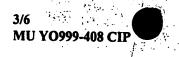


FIG.4



ANNEAL TO FORM CoSi2

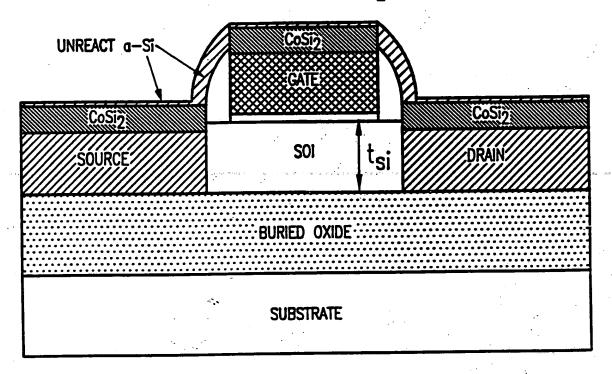


FIG.5

ETCH UNREACTED a-Si

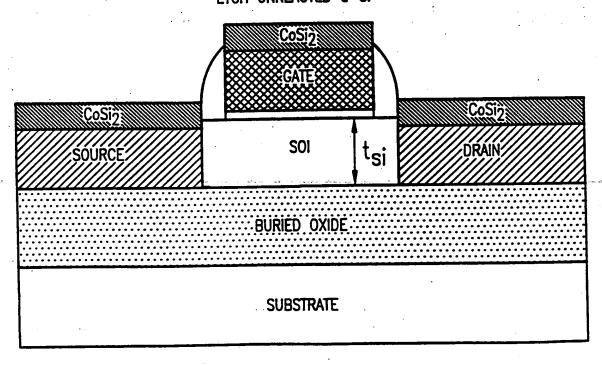
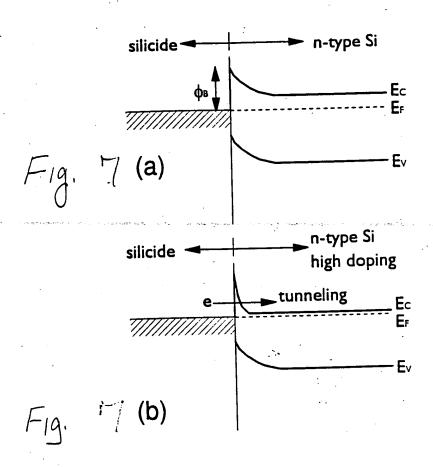
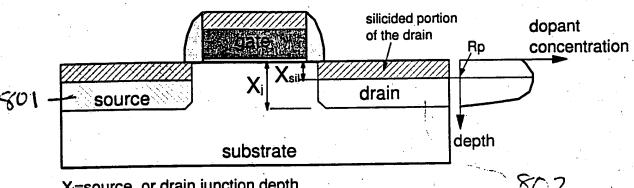


FIG.6





X=source or drain junction depth

Xa=silicide junction depth

Rp = peak dopant concentration

Requirements:

1. X_i>X_{sii}

2. Xst roughly equals Rp

Figure

